

Attorney Docket No.: TRAN-P004D

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

· Patent Application

Inventor(s):

Kelly, et al.

Group Art Unit:

2186

Filed:

10/30/2000

Examiner:

Thai, Tuan V.

Application No.:

09/699,947

Title:

TRANSLATED MEMORY PROTECTION APPARATUS FOR AN ADVANCED

MICROPROCESSOR

Form 1449

U.S. Patent Documents

Examiner Initial	No.	Patent No.	Date	Patentee	Class	Sub- class	Filing Date
	A						
	В						
	С						- · · · ·
	D		<u> </u>			<u> </u>	
	E						
	F					- 	
	G						
	Н						
	1.				-		
	J		1		· · · · · · · · · · · · · · · · · · ·	+	
	K					+	+

Foreign Patent or Published Foreign Patent Application

Examiner Initial	No.	Document No.	Publication Date	Country or Patent Office	Class	Sub- class	Translation	
							Yes	No
	L	1				1		1
	M			·		+		\vdash
	N					 	 	
	0						+	 -
	Р					- 	 	
	Q						+	

Other Documents

		Other Documents
Examiner Initial	No.	Author, Title, Date, Place (e.g. Journal) of Publication
Th.	R	Bedichek, R. C., Talisman: FAST AND ACCURATE MULTICOMPUTER SIMULATION, MIT 1995.
	S	
	T	
Examiner	Tu	au V. Than Date Considered 06/14/27

Examiner: Initial citation considered. Draw line through citation if not in conformance and not considered.

Include copy of this form with next communication to applicant.